

FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.  
AL-223APPLICATION NO.  
10/099,707SUPPLEMENTAL INFORMATION DISCLOSURE  
STATEMENT BY APPLICANTSAPPLICANTS  
Andrasic et al.CONFIRMATION NO.  
2004FILING DATE  
March 13, 2002GROUP  
~~2817~~ 2816

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
<i>u</i>	5,121,014	06/1992	Huang	307	605	
<i>u</i>	5,208,557	05/1993	Kersh, III	331	57	
<i>u</i>	5,952,891	09/1999	Boudry	331	57	

## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
<i>u</i>	EP 0 987 822 A2	03/2000	Europe	H03L	7/099		
<i>u</i>	EP 1 056 207 A1	11/2000	Europe	H03L	7/099		
<i>u</i>	10,215,156	08/1998	Japan	H03K	5/13		

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER INITIAL	

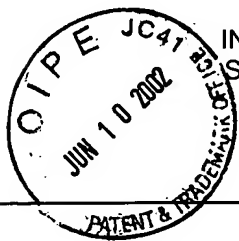
EXAMINER

DATE CONSIDERED

3/12/2003

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not conformance and not considered. Include copy of this form with next communication to applicants.

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U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.  
174/223SERIAL NO.  
10/099,707APPLICANT  
Stjepan W. Andrasic  
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## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
<i>n</i>	3,473,160	10/14/69	Wahlstrom	340	172.5	
<i>n</i>	U.S. patent application no. 09/805,843	PD 3/13/01	Aung et al.	—	—	3/13/01

## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER INITIAL	
<i>n</i>	"ORCA ORT82G5 1.0-1.25/2.0-2.5/3.125 Gbits/s Backplane Interface FPSC", Preliminary Data Sheet, July 2001, Agere Systems Inc., pp. 1-35.
<i>n</i>	"ORCA ORT8850 Field-Programmable System Chip (FPSC) Eight Channel x 850 Mbits/s Backplane Transceiver", Product Brief, July 2001, Agere Systems Inc., pp. 1-6.
<i>n</i>	"ORCA ORT8850 Field-Programmable System Chip (FPSC) Eight Channel x 850 Mbits/s Backplane Transceiver", Data Sheet, August 2001, Agere Systems Inc, pp. 1-36.

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